

FIG.1

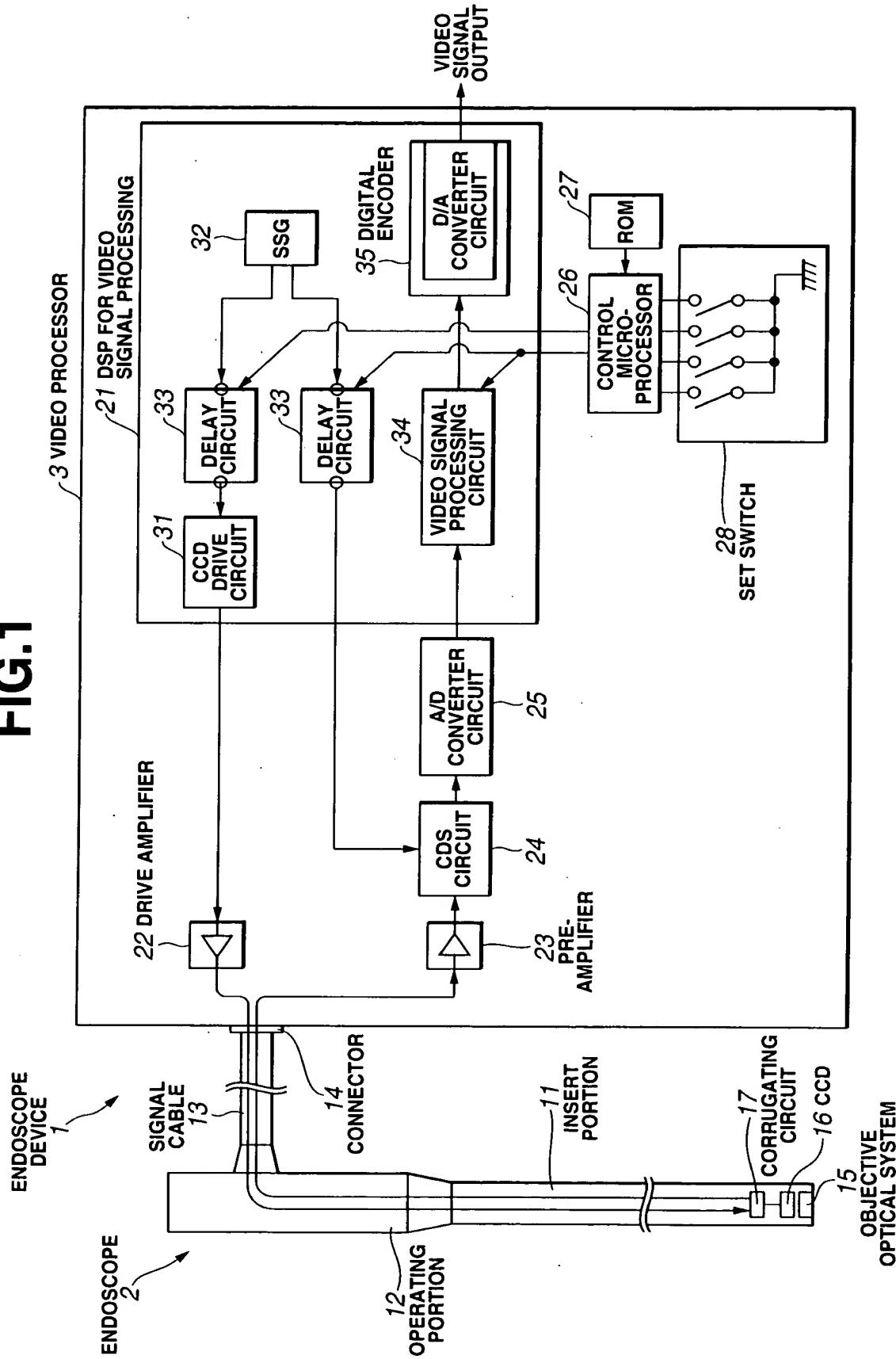


FIG.2

33 DELAY CIRCUIT

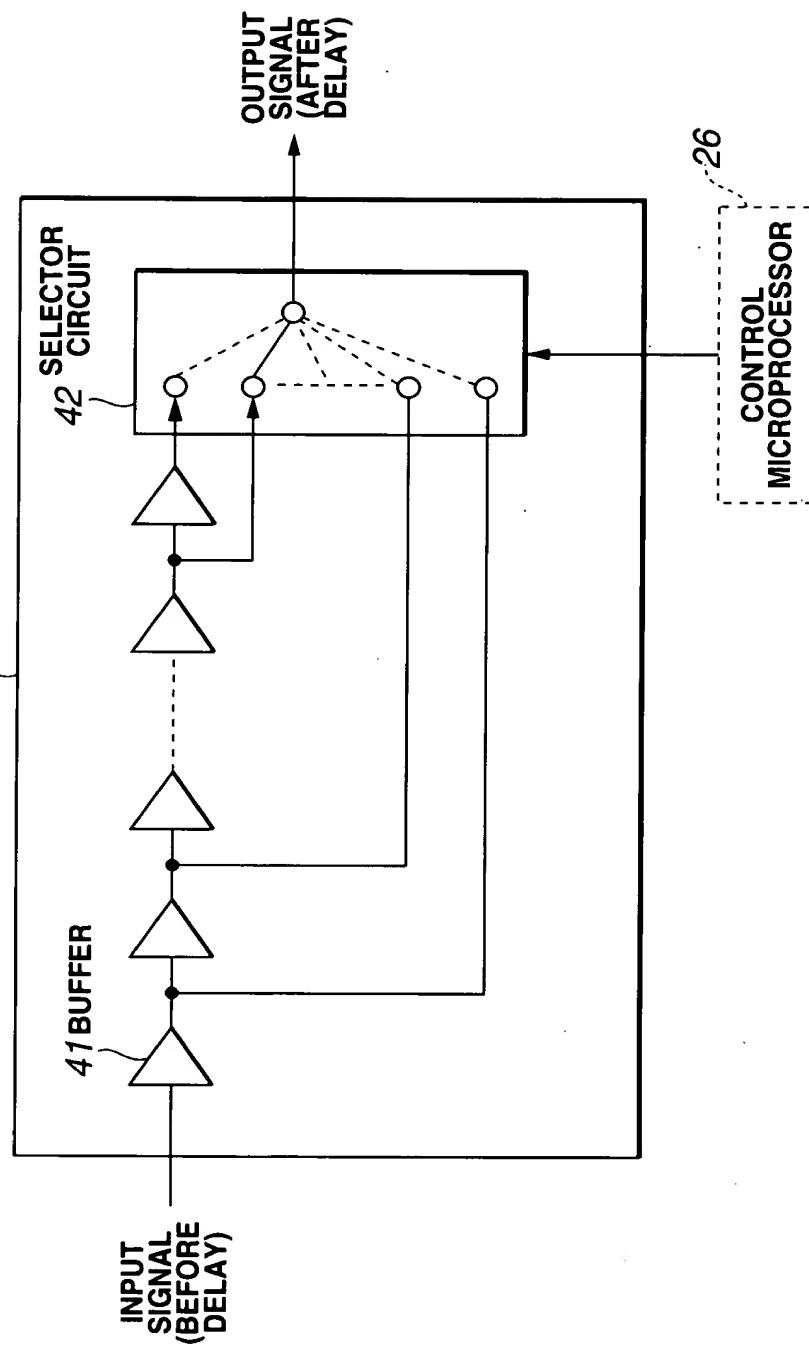


FIG.3

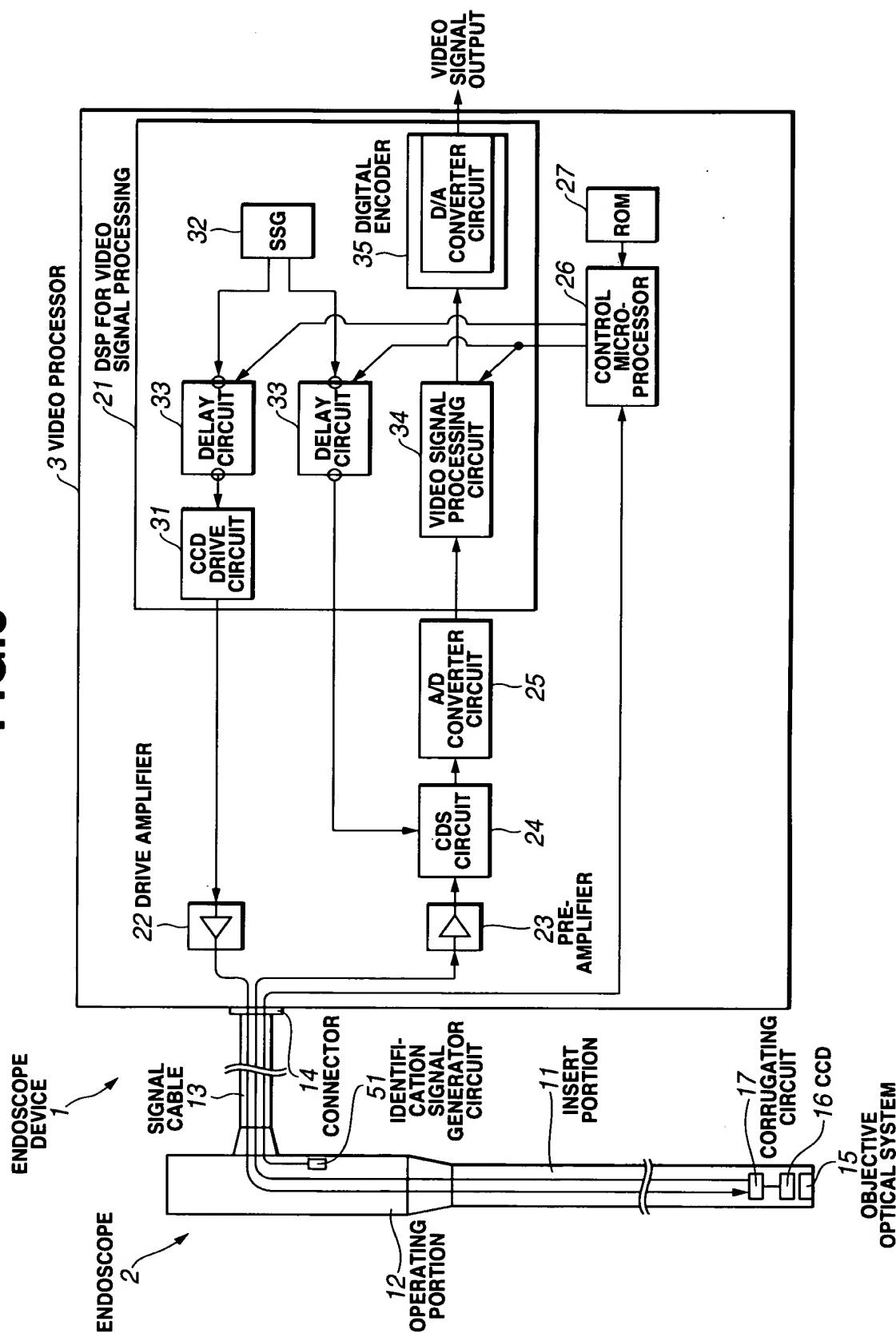


FIG.4

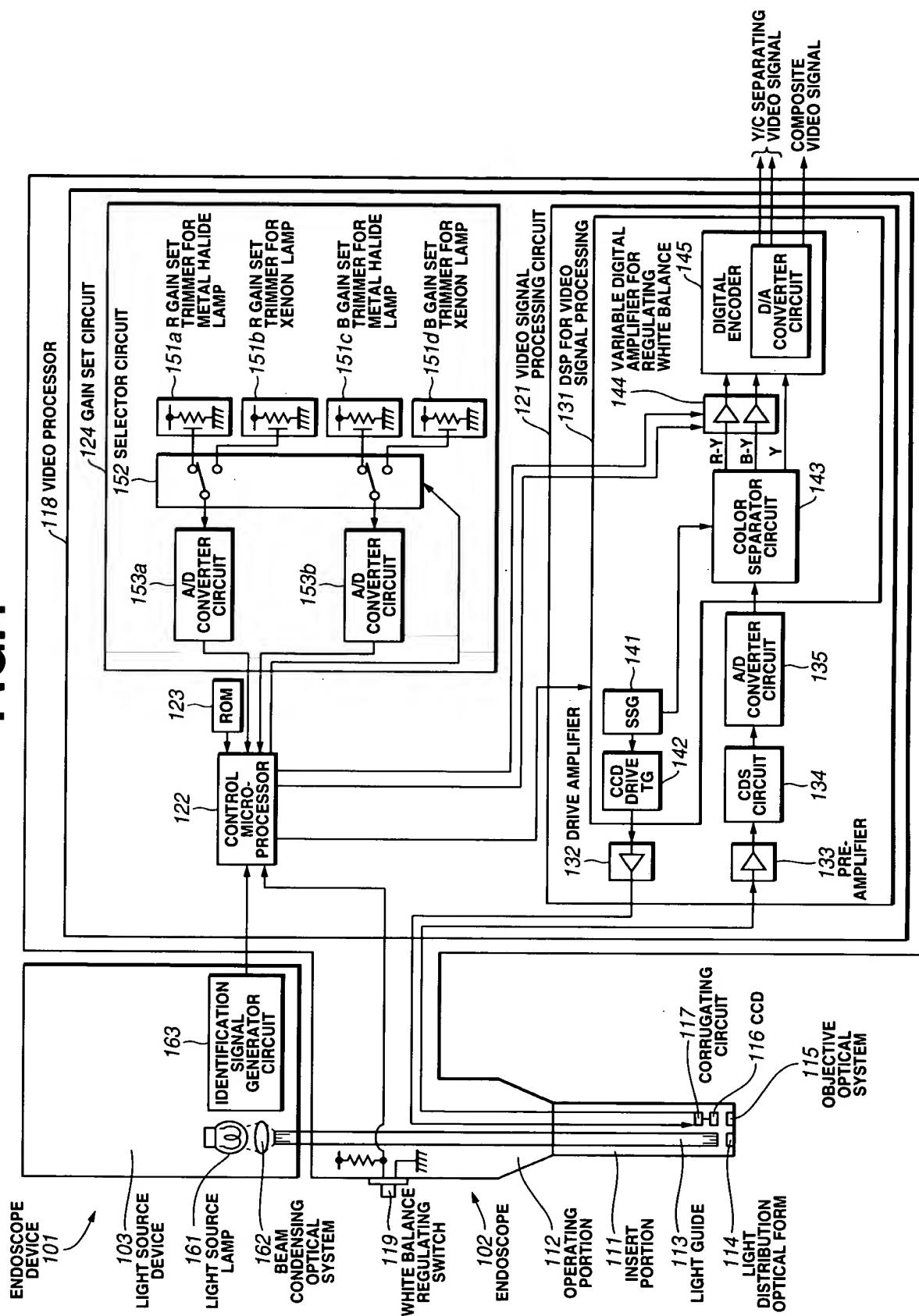
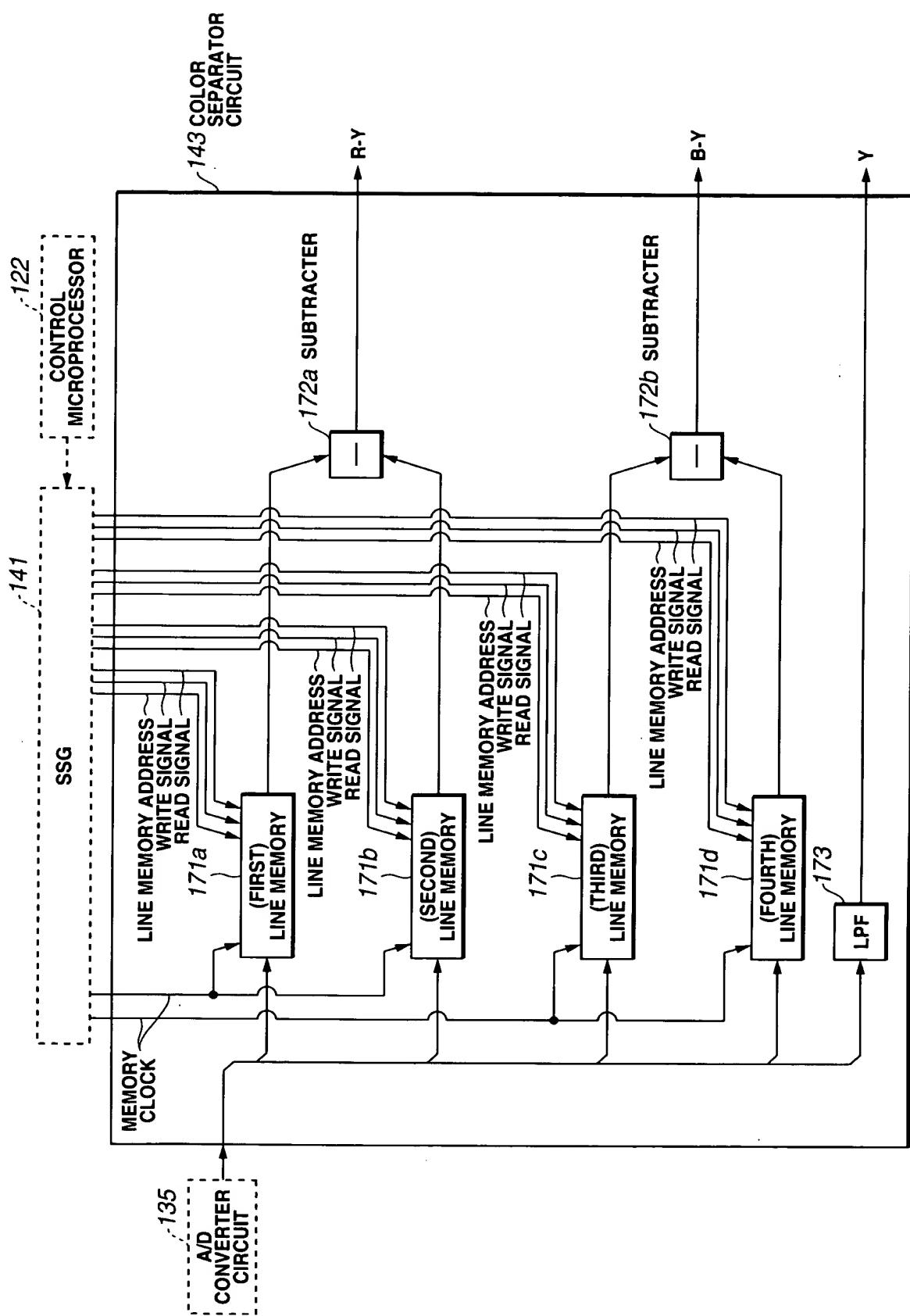


FIG.5



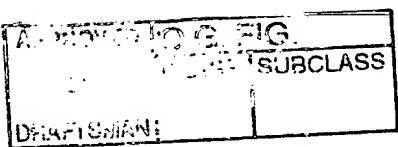
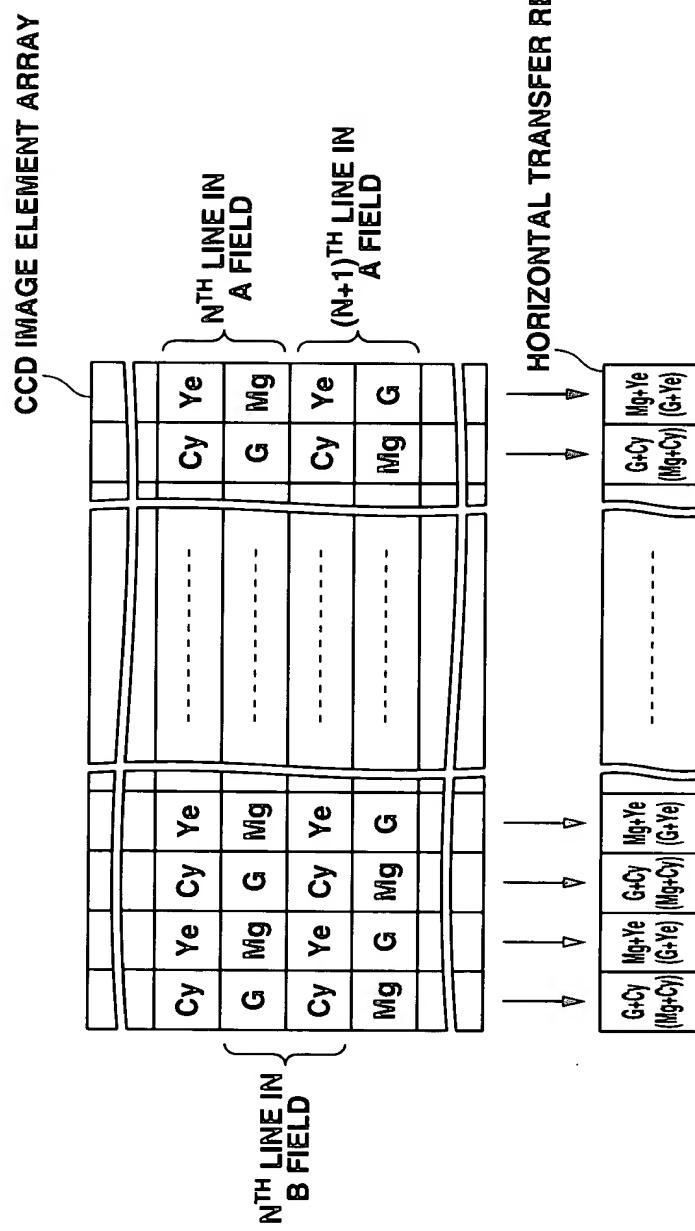
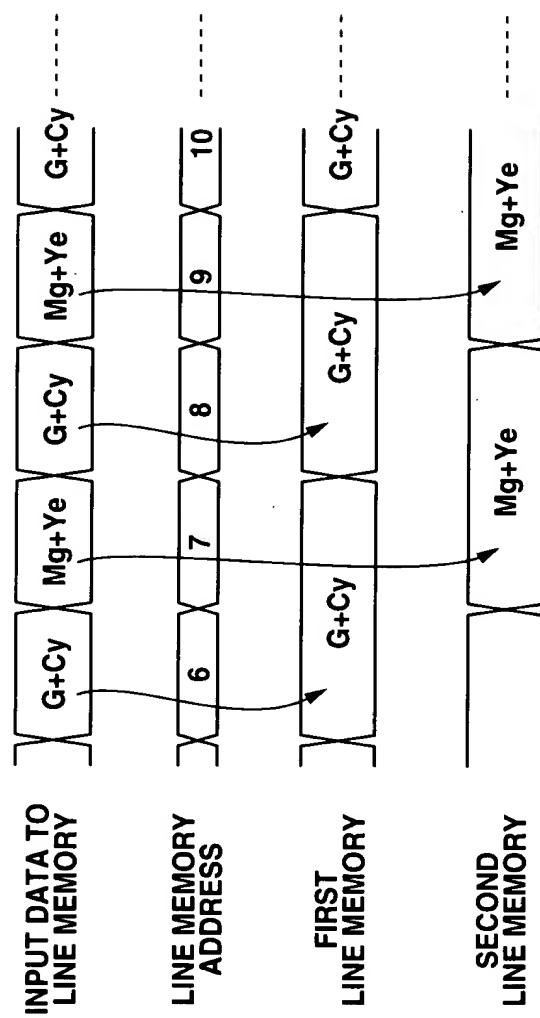


FIG. 6



APPLIED FOR	FIG.
SEARCHED	SUBCLASS
DRAFTSMAN	

FIG.7



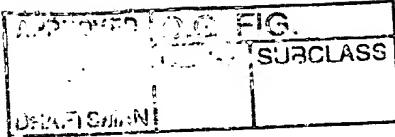


FIG.8(A) OPERATION WITHOUT DELAY

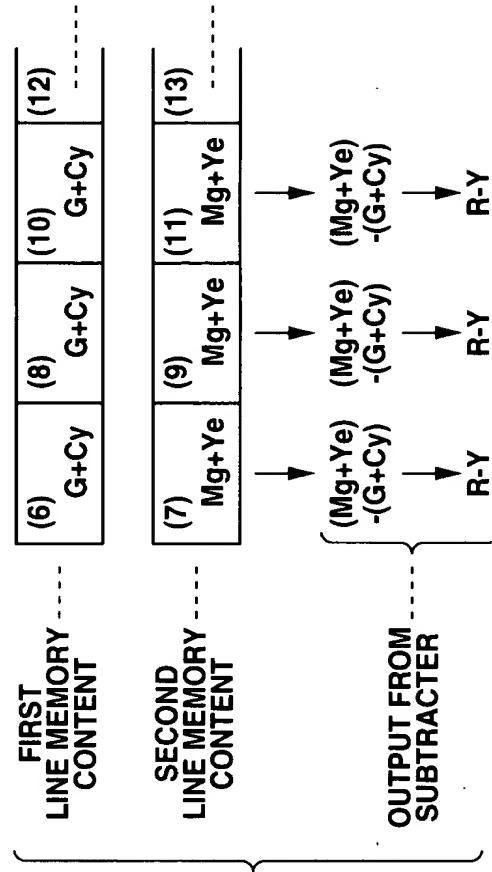


FIG.8(B) OPERATION WITH ONE PIXEL DELAY

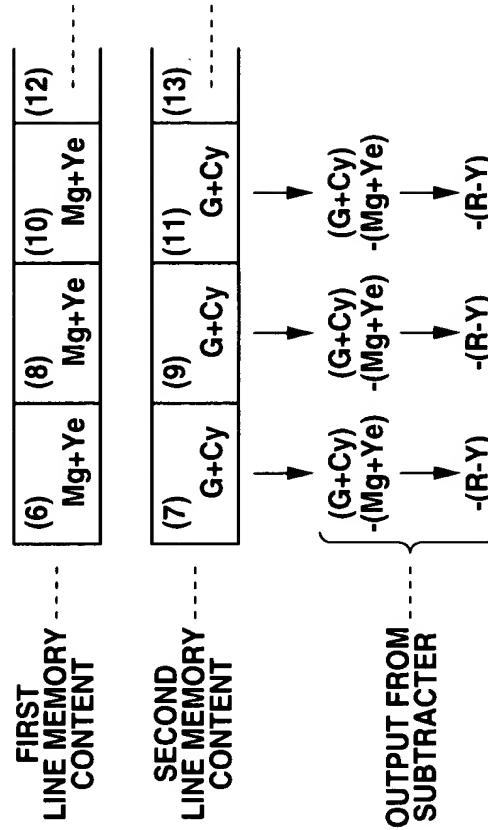
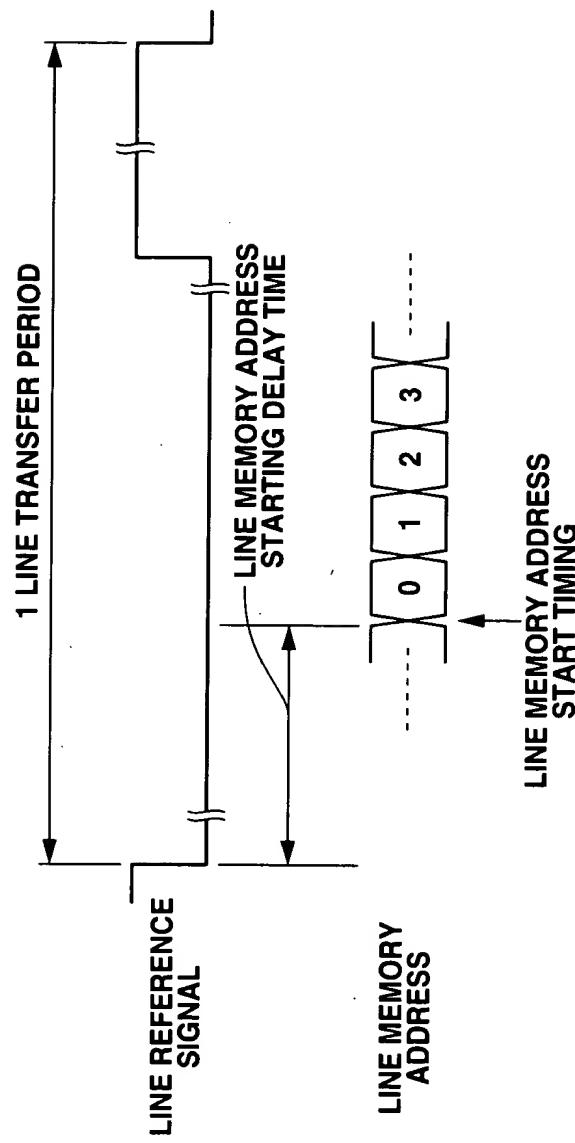


FIG.9



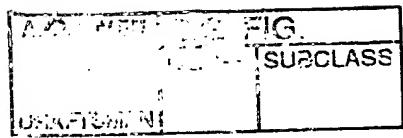


FIG.10

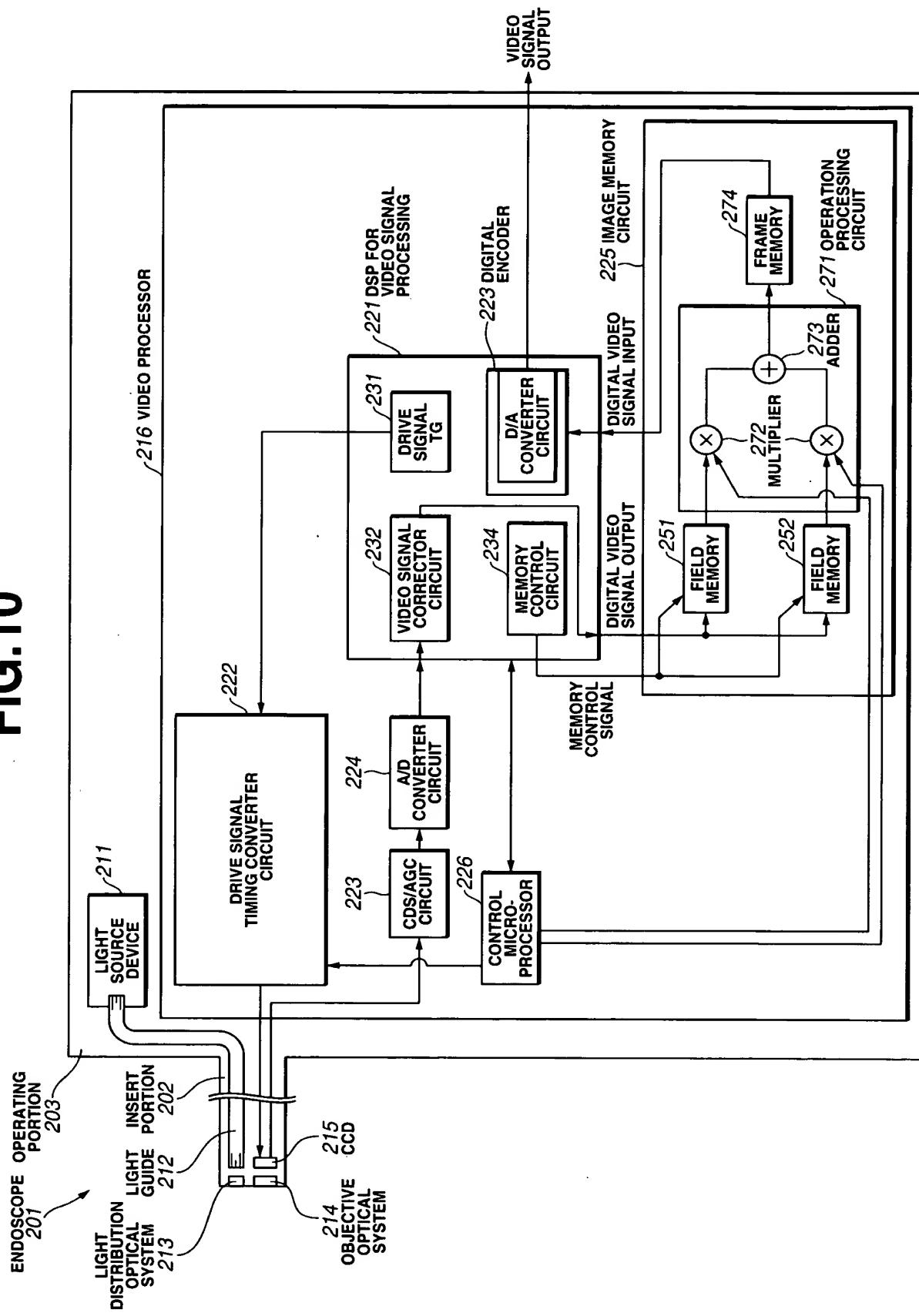
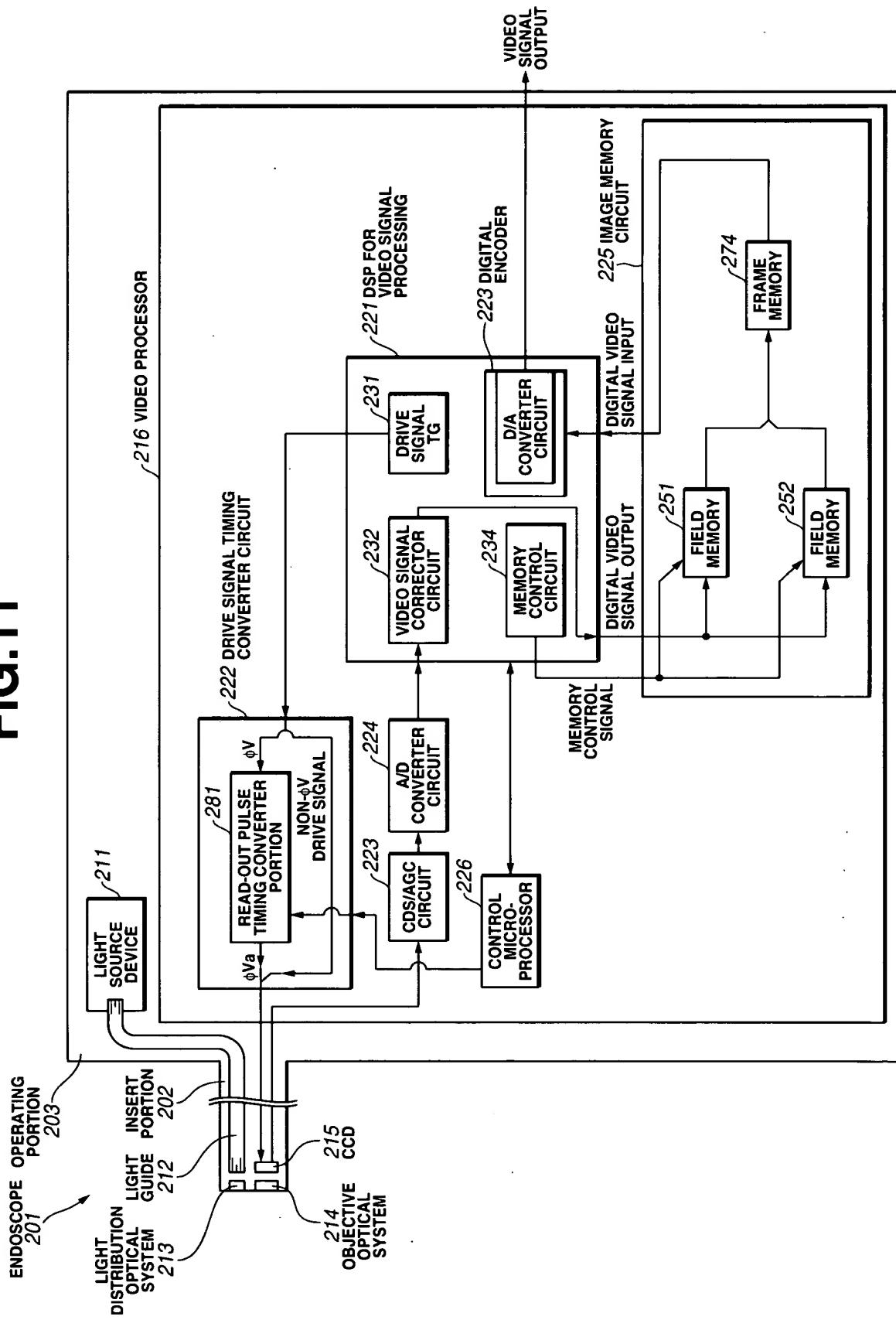


FIG.11



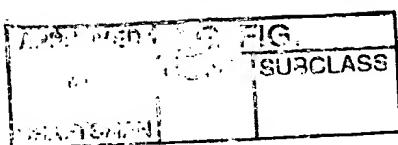


FIG.12

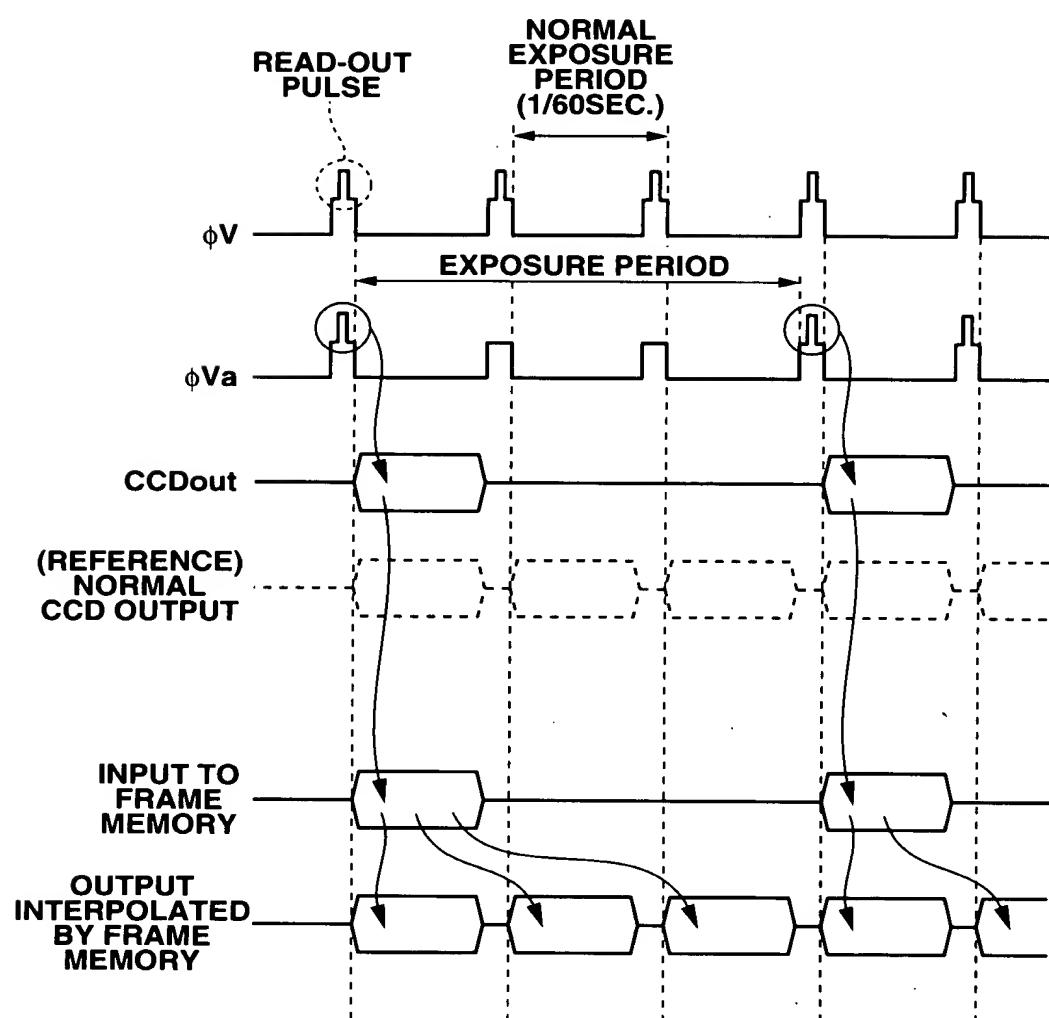


FIG.13

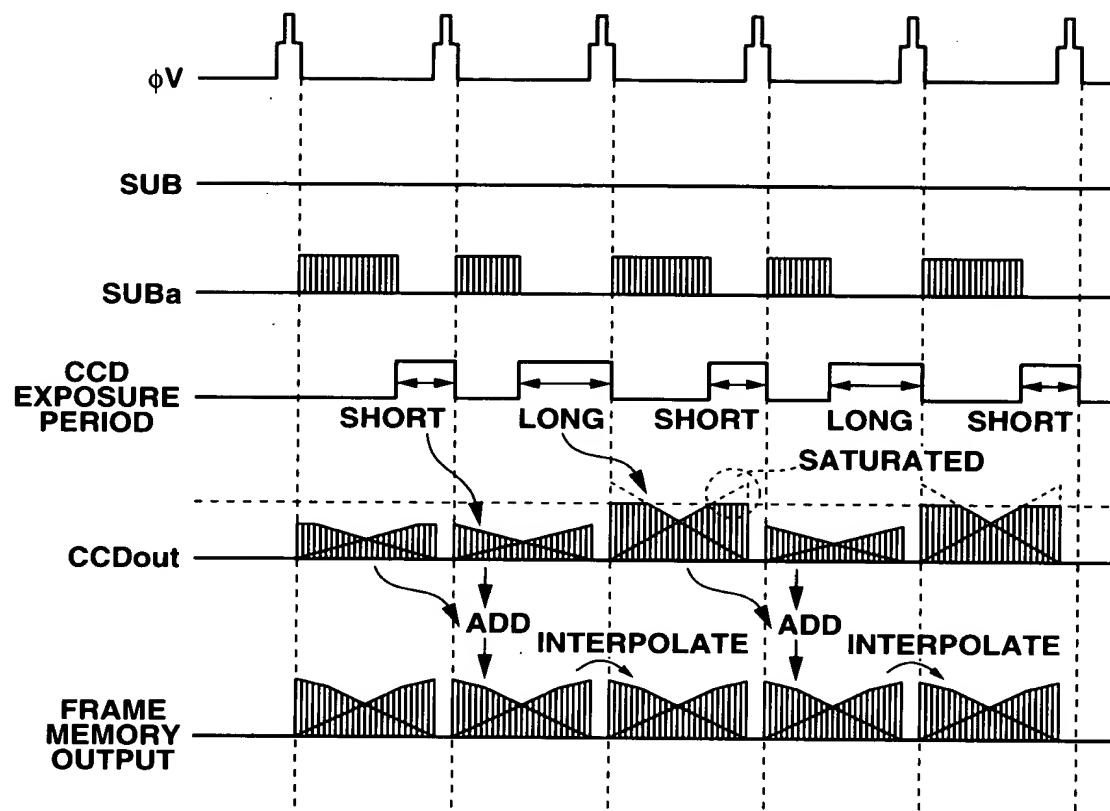


FIG.14

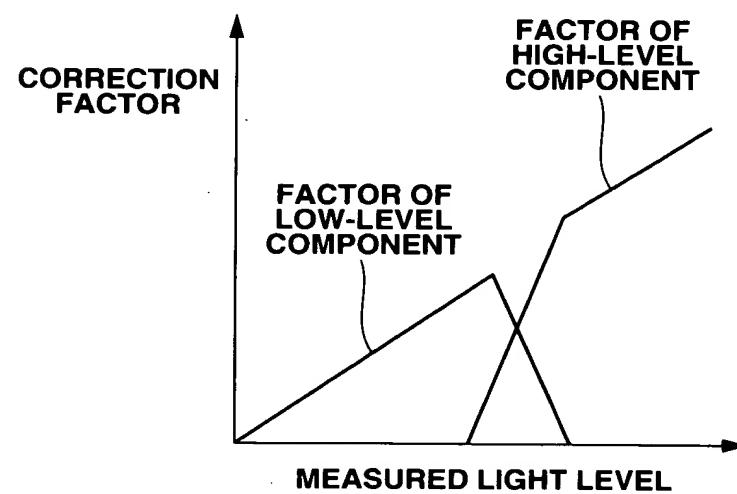


FIG. 15

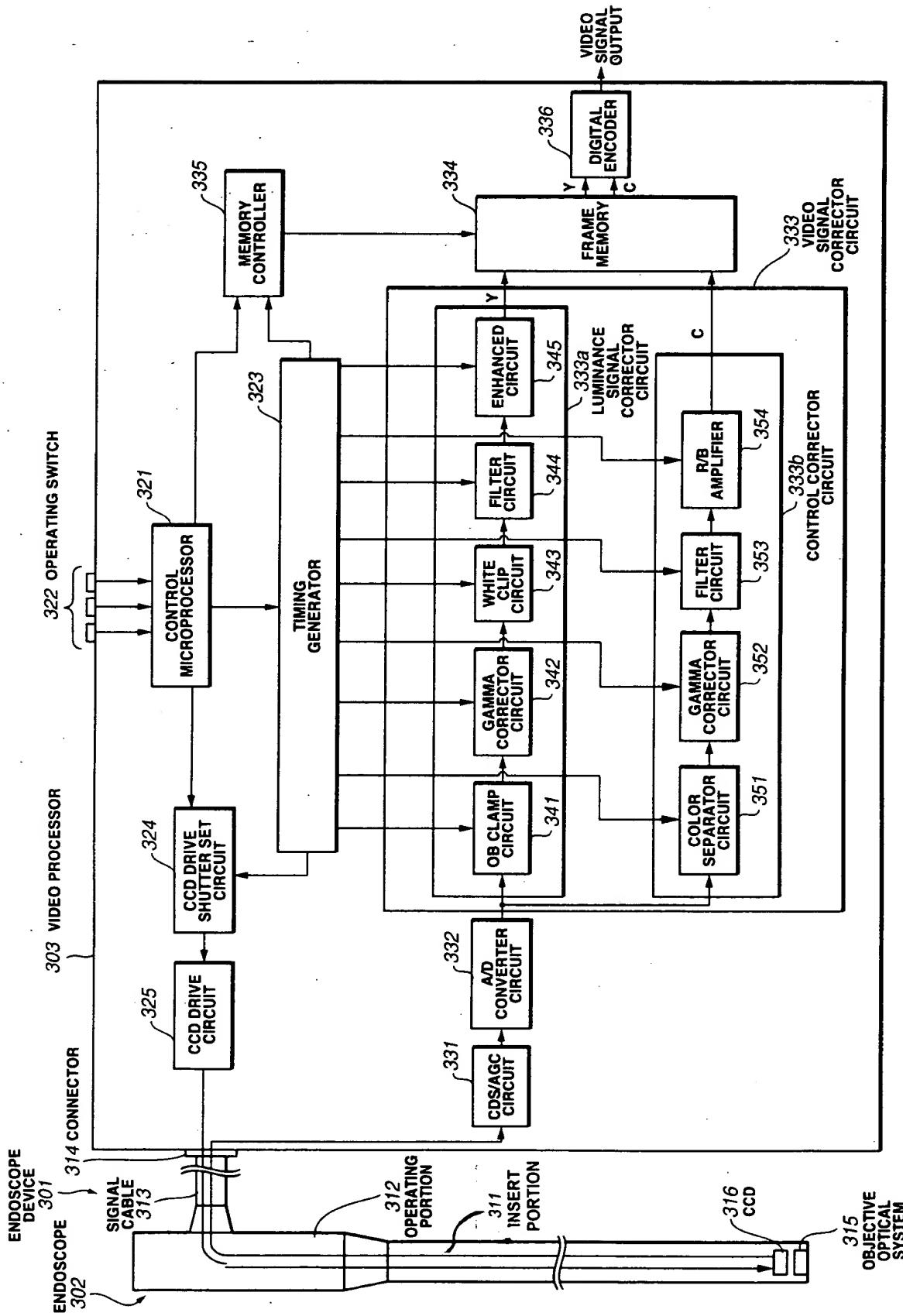


FIG.16

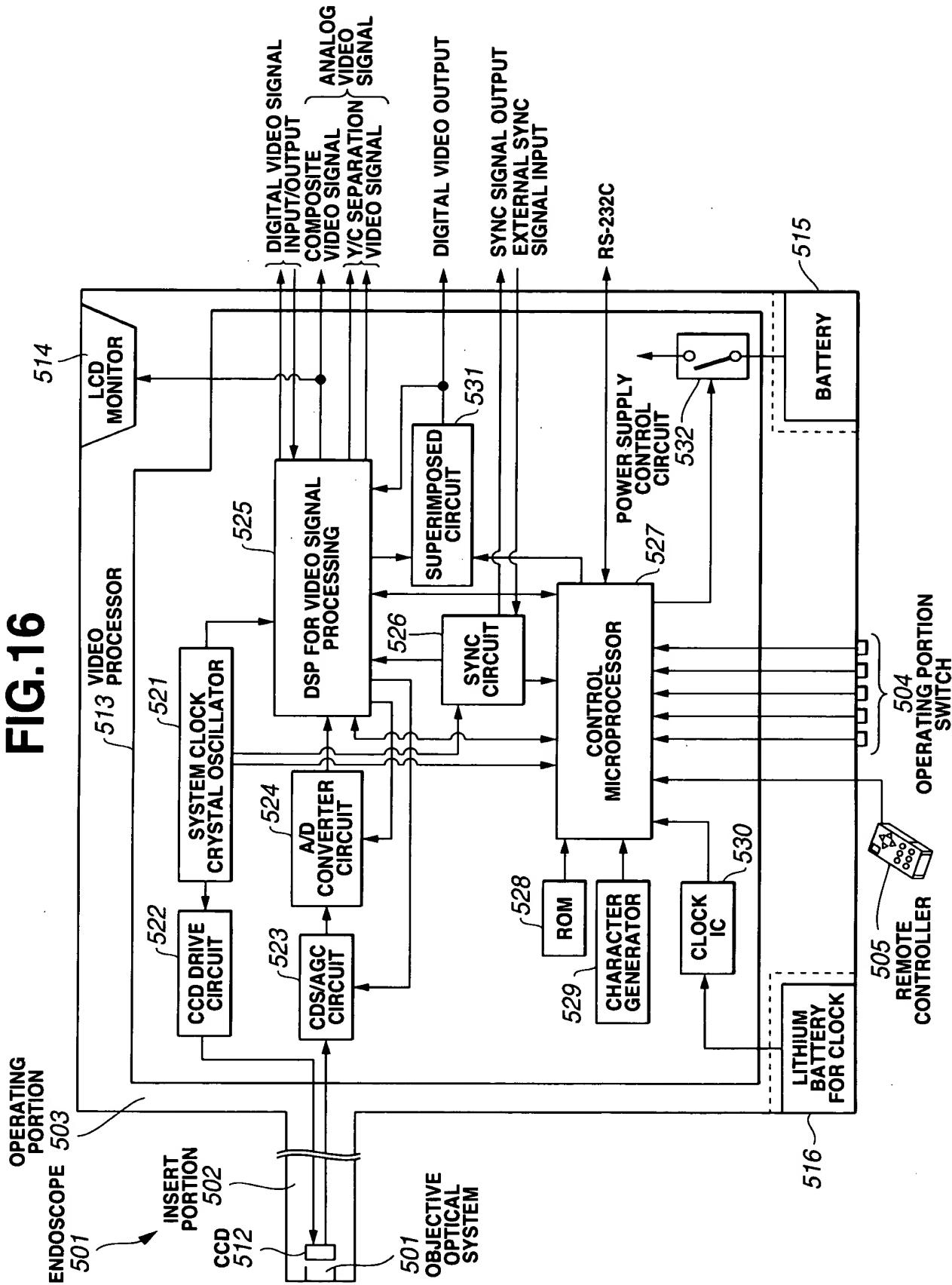


FIG.17

